Topology Optimization in Cellular Neural Networks

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Abstract— **This paper presents a constrained combinatorial optimization approach to the design of cellular neural networks with sparse connectivity. The method applies to cases where maintaining links between neurons incurs a cost, which could possibly vary between these links. The interconnection topology of the cellular neural network is diluted without significantly degrading its performance, the latter quantified by the average recall probability for the desired patterns engraved into its associative memory. The dilution process selectively removes the links that contribute the least to a metric related to the size of system's desired memory pattern attraction regions. The metric used here is the magnitude of the network's nodes' stability parameters, which have been proposed as a measure for the quality of memorization. We demonstrate by means of an example that this method of network dilution produces cheaper associative memories that in general trade off performance for cost, and in many cases the performance of the diluted network is on par with the original system.**

Index Terms— **Cellular neural networks, stability parameters, sparse associative memory.**

I. Introduction

A cellular neural network (CNN) is a nonlinear dynamical system, implementing an associative memory, where neuron interactions are limited to the neighboring units only [1]. Research on the design and synthesis of these types of networks has gained increasing attention in recent years because of their topology being relatively sparse, which makes them a promising choice for very-large-scale integration implementations. Several synthesis procedures for designing symmetric/non-symmetric neural network have been proposed [2]–[4], and one of the main directions of current research on the topic is the stability behavior of these systems in the presence of time delays (see for example [5], [6]), which is however beyond the scope of this paper.

Strengths and weaknesses of several network design techniques are discussed in [2]. The analysis results shown in [3] allow one to locate in a systematic manner all equilibrium points of the neural network and to determine the stability properties of the equilibrium points. Different design methods are generalized in [7] in a design procedure for neural networks with sparse connectivity. These results guarantee that the synthesized neural networks have predetermined sparse interconnection structures and store an almost arbitrary set of desired memory patterns as reachable memory vectors. The authors show that a sufficient condition for the existence of such a sparse neural network design is self feedback for every neuron in the network. Critical network issues like robustness and invariance to perturbation have been addressed in [8] where the authors provide upper bounds for the perturbations of parameters under which desired memories stored in a neural network are preserved. This type of information is of great practical interest during the implementation process of such networks. In [4] the problem of realizing associative memories by designing a CNN that can store given binary vectors with improved performance is formulated as a constrained optimization problem, which can be reduced to a generalized eigenvalue problem (GEVP) [9]. Networks designed using this method exhibit less spurious patterns and higher recall probabilities [4]. Similar interior point methods are also applied to the problem of synchronization in chaotic delayed neural networks [18], [19].

The quality of memorization in associative memories is typically measured by the *average recall probability* [4], [8], which is an approximation of the probability that the network will recall correctly one of the patterns stored in its memory, if presented with a version of this pattern contaminated by noise. This number is defined as the ratio of number of recovered memory patterns (perturbed initial condition vectors which result in same output as the stored memory vector) to the total number of perturbed initial condition vectors. Although this quantification method measures the network's performance accurately and unambiguously, it can only be applied after the network has been designed and tested on a significant number of test inputs. In other words, it does not allow the designer to *predict* the network's performance without experimentation.

The concept of stability parameters appears in some earlier work on associative memory networks [10]–[12], as a measure of quality of memorization. Specifically, it has been demonstrated [13], [14] that the sign as well as the magnitude of these numbers are related to the size of the attraction regions of the desired memory patters. Although the applicability of this concept as a universal measure of memorization quality, (specifically, the use of their size as a direct measure of the absolute sizes of the attraction regions) has long been debated [15], these parameters are generally accepted as a metric for the network's performance [16].

Reported approaches on the design of CNNs (without time delays) start with, and build on some given network topology. The choice of this starting point is justified from the fact that typically the physical platform on which the network is implemented is fixed. In this paper, we raise the question of how could the design process be different if the network topology was also part of the design. It could be the case that implementing and maintaining certain neuron connections might be more expensive than others, and then one may be forced to strike a balance between connectivity and performance. Simple stability analysis of

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neural networks implementing associative memories suggests similar links between the network of connections and the stability of the dynamical system as in consensus networks. In the case of neural networks, however, the topologies used are typically standard: either complete graphs (in Hopfield networks) or grid-like structures (in CNNs). Yet, available design methodologies do not place specific conditions on the network's structure. In addition, the cost of communication (delayed or otherwise) between neurons is commonly ignored. But if neurons communicate at a nontrivial cost, there is benefit in designing networks that perform just as well, but are less expensive.

This paper presents an approach to the optimization of the network topology of CNNs in which communication links between neurons may incur variable cost. We selectively "trim" network links in an effort to trade network performance for smaller communication cost. Based on existing design tools, we perform combinatorial optimization on a portion of a CNN that includes the *most expensive* interconnection links. This produces a sparser CNN, the performance of which can be comparable to the original network. This performance is quantified in terms of the network recall probability, and in the proposed optimization algorithm approach is captured by the neural network's stability parameters.

II. Preliminaries

A two-dimensional continuous time zero-input CNN consisting of $n = MN$ cells in a $M \times N$ array, as first introduced by Chua and Yang [1], can be described by

$$\dot{x}_{ij} = -x_{ij} + \sum_{(k,l)\in N_r(i,j)} W_{ij,kl} y_{kl} + d_{ij}, \quad y_{ij} = \operatorname{sat}(x_{ij}),$$

where $1 \le i \le M$, $1 \le j \le N$, and $\operatorname{sat}(x_{ij}) \triangleq \frac{1}{2}(|x_{ij}+1| - |x_{ij}-1|)$. Here x_{ij} and y_{ij} are the state and output of the (i,j)th cell respectively, and $N_r(i,j)$ is an r-neighborhood of the (i,j)th cell defined as $N_r(i,j) \triangleq \{(k,l) : \max\{|k-i|, |l-j|\} \le r\}$, for $1 \le i \le M, 1 \le j \le N$. This system can be expressed as

$$\dot{x} = -x + T \operatorname{sat}(x) + b, \tag{1a}$$

$$y = \operatorname{sat}(x) \tag{1b}$$

where $x = [x_{11}, x_{12}, \ldots, x_{21}, \ldots, x_{MN}]^T \in \mathbb{R}^n$ is the stack vector of all neuron states , $y = [y_{11}, y_{12}, \ldots, y_{21}, \ldots, y_{MN}]^T \in H^n$ is the output vector (H^n is the n-dimensional hypercube $[-1,+1]^n$), $T = [T_{ij}] \in \mathbb{R}^{n\times n}$ is the network connection weight matrix, $b \in \mathbb{R}^n$ is the network's bias vector and for vector arguments, the saturation function is defined elementwise.

Let B^n denote the set of bipolar vectors in H^n, namely those whose elements are either $+1$ or -1. For $i = 1, \ldots, n$, the initial condition vectors of (1) should always satisfy $|x_i(0)| \le 1$. The interconnection topology of the network can be described by an adjacency matrix S, and thus a weight T_{ij} is non-zero only if $S_{ij} = 1$. Vector $\alpha \in H^n$ is a *memory vector* for (1) if the latter has an asymptotically stable equilibrium point $\beta \in \mathbb{R}^n$ such that $\alpha = \operatorname{sat}(\beta)$ [1].

The synthesis problem for a CNN can be stated as follows:

Problem 1 (Synthesis): Given a CNN (1), implemented on network expressed by an adjacency matrix S, along with the set of desired bipolar memory vectors $\alpha^1, \ldots \alpha^m \in B^n$, determine the network connection weights T_{ij} and bias parameters b_i so that the obtained neural network can store all desired memory patterns as reachable memory vectors.

The adjacency matrix S which determines which T_{ij}, typically encodes a lattice structure and can be selected arbitrarily as long as certain conditions are satisfied [1], [3], [4]: If $\alpha \in B^n$ and $\beta = T\alpha + b$ are such that

$$\alpha_i\beta_i = \alpha_i\big(\sum_{j=1}^{n} T_{ij}\alpha_j + b_i\big) > 1, \quad \forall i = 1, \ldots, n, \tag{2}$$

then (α, β) is a pair of a memory vector and an asymptotically stable equilibrium point of system (1). Also if $\alpha \in B^n$ and $\beta = T\alpha + b$ are such that for any $i = 1, \ldots, n$, $\alpha_i\beta_i = \alpha_i\big(\sum_{j=1}^{n} T_{ij}\alpha_j + b_i\big) < 1$, then $\alpha \in B^n$ cannot be a memory vector. System (1) is globally stable if T is symmetric.

In addition to aforementioned stability criteria, specific stability and robustness properties for these networks are established in terms of the elements of the network's weight matrix T and bias vector b [1], [3], [4]:

Let $\alpha \in B^n$ be a memory vector of system (1) and let $k \ge 1$ be an integer. If $\widetilde{T} = T - I_n$ and b satisfy

$$\alpha_i \left(\sum_{j=1}^{n} (\widetilde{T}_{ij}\alpha_j + b_i) \right) > 2(k-1) \max_{1\le j\le n} \left|\widetilde{T}_{ij}\right|, \tag{3}$$

for $i = 1, \ldots, n$, then any binary vector $\alpha^* \in B^n$ such that $1 \le h(\alpha^*, \alpha) \le k$ ($h(\alpha^*, \alpha) \triangleq \sum_i |\alpha_i^* - \alpha_i|$ denotes the Hamming distance) has the following properties:

1) α^* is not a memory vector (asymptotically stable equilibrium point for (1)).
2) if $x(0) = \alpha^*$ and $\alpha_i^* \ne \alpha_i$, then $x_i(t)$ converges to α_i.

Satisfying (3) with large k increases both the attractivity and the robustness of the stored memory vector $\alpha \in B^n$ and decrease the probability of existence of spurious patterns in vertices near α [4].

Problem 1 can be formulated formulated as a GEVP [4]. For $i, j = 1, \ldots, n$, and $k = 1, \ldots, m$,

$$\min(-\delta), \quad \text{s.t.} \tag{4a}$$

$$(-\delta)\operatorname{diag}[2q_1, \ldots, 2q_n] - \operatorname{diag}[-p_1, \ldots, -p_n] > 0 \tag{4b}$$

$$\alpha_i^{(k)} \left(\sum_{j=1}^{n} \widetilde{T}_{ij}\alpha_j^{(k)} + b_i \right) - p_i > 0, \tag{4c}$$

$$q_i - \widetilde{T}_{ij} > 0, \tag{4d}$$

$$\widetilde{T}_{ij} + q_i > 0, \tag{4e}$$

$$\widetilde{T}_{ii} = 0, , \tag{4f}$$

$$\widetilde{T}_{ij} = \widetilde{T}_{ij}^T = \widetilde{T}_{ij}|_S \tag{4g}$$

$$L < q_i < U, \tag{4h}$$

where p_i and q_i for $i = 1, \ldots, n$ are additional slack

variables used to cast the design problem as a linear matrix inequality (LMI) [17], and L and U are the lower and upper bounds for the design variables in the GEVP.

In this paper we quantify the quality of memorization in the network in terms of the magnitude of the stability parameters $K_{i\mu}$ defined as:

$$K_{i\mu} = \alpha_i^{(\mu)} h_i = \frac{\alpha_i^{(\mu)} \sum_j c_{ij} \alpha_j^{(\mu)}}{||c_i||_2}, \tag{5}$$

where $c_{ij} = T_{ij}$, $\|c_i\| = \sqrt{\sum_j c_{ij}^2}$, and superscipt $(\cdot)^{(\mu)}$ indexes the set of desired memory vectors. The stability parameters $K_{i\mu}$, (one for each pair of neuron node and memory vector) are numbers which have been proposed as a measure of quality of memorization and it has been hypothesized that they are linked to the size of the attraction regions of the neural network [12], [15], [16]. The stability parameters and the degree of symmetry of connection matrix control the system dynamics and the domain sizes. Positive values of stability parameters indicate stability of a pattern.

The problem addressed in this paper is the following variant of Problem 1:

Problem 2 (Topology optimization): Given (1), implemented on network expressed by a *weighted* graph with adjacency matrix $\hat{S}$, with the set of desired bipolar memory vectors $\alpha^1, \dots \alpha^m \in B^n$, determine the connection weights T_{ij} and bias parameters b_i of a subnetwork of S, so that this subnetwork stores all desired memory patterns as reachable memory vectors, and recalls them (almost) as well as the complete network.

In this paper, the performance metric is formulated using the values of the neural network's stability parameter values. The design process includes an optimization stage, in which the high cost edges that contribute the least to the engraving of the desired patterns on the network's memory, are selectively trimmed.

III. Dilution of Network Connectivity

A CNN can be viewed as a collection of sub-networks that are linked to each other through communication links. We assume that communication incurs a cost, and in a particular scenario of interest, information flow across sub-networks is more expensive compared to communication within each subnetwork. In this case, one may be interested in minimizing communication cost, while maintaining the functionality and performance of the whole network above a certain threshold.

In this paper, reducing communication cost is achieved by trimming those expensive neuron links which do not contribute significantly to the ability of the network to retrieve its memorized information. The communication costs are captured by the weights of the weighted adjacency matrix $\widehat{S}$. Given the (unweighted) adjacency matrix S of the CNN, along with the set of desired bipolar memory vectors $\alpha^1, ...\alpha^m \in B^n$, the design process begins by determining the network parameters T_{ij} and b_i through the solution of the GEVP (4). The resulting network maximizes the recall probability of the patterns it has been designed for, without considering the cost of using the different network links. The next step is to dilute the connectivity of S, by judiciously removing some of the high cost links identified in $\widehat{S}$, in a way so that the quality of memorization is not severely affected. Balancing performance against communication cost is achieved through (combinatorial) optimization over the network links, subject to the stability constraints stated in Section II.

The optimization process is iterative. For a given (intermediate) topology S, the algorithm determines the neural network weights T_{ij} for $i, j = 1 \dots, n$ and biases b, and based on the selected patterns $\alpha_i^{(\mu)}$ for $\mu = 1, \dots, m$ to be memorized, an $n \times m$ stability parameter matrix is formed by repeated application of (5): $K = [K_{i\mu}]_{i=1,\dots,n;\mu=1,\dots,m}$. For the prescribed patterns $\alpha^1 ... \alpha^m$ to be stored effectively in the network's memory, all stability parameters $K_{i\mu}$ must be nonnegative and as large as possible. For the network topology encoded in S, the value of the following objective function is evaluated:

$$\bar{K} = \sum_{i=1}^{n} \sum_{\mu=1}^{m} K_{i\mu}. \tag{6}$$

Thus $\bar{K}$, being the sum of all nodes' stability parameters for all chosen memory vectors, quantifies the collective ability of the network to recall all desired memories.

Remark 1: Several performance metrics have been explored as alternatives to (6), based on different norms of the stability parameter matrix K, such as the minimum row (or column) sums, the (absolute) minimum element of K, *etc*. When the average recall probability of each design was evaluated, it was determined that the sum of all stability parameters captured more accurately the ability of the network to recall memory patters.

On the other hand, a naive implementation of a "branch-and-bound" approach, where links are divided into "promising" and "not-promising" for deletion groups according to their associated $\bar{K}$ value, and only the "promising" possibilities are explored in the subsequent steps, will generally fail. This is because due to the combinatorial nature of the problem, an edge whose sole deletion has an adverse effect on the stability parameters may even improve the value of $\bar{K}$ when combined with additional edge removals.

For a given cost threshold ν, and performance threshold κ, the high-cost edges that are candidates for deletion are identified in the residual adjacency matrix

$$R = \frac{1}{2}\left(\text{sign}(\hat{S} - \nu S) + S\right),$$

where the sign function is evaluated element-wise on the matrix argument. For every nonzero (i, j) element in R, we remove the associated (i, j) edge from S. If (3) is satisfied then we store the resulting value of $\bar{K}$. The high-cost edge associated with the highest stored $\bar{K}$ value is marked for deletion, and the step is repeated for the topology S', $\hat{S}'$ in which neurons i and j are not linked.

The process for evaluating the edges which are candidate

Algorithm 1 Topology optimization

Require: Matrices α, S, $\widehat{S}$, constants ν, κ.
Ensure: Matrix A of $\bar{K}$ values for each edge that may be removed.

$R \leftarrow \frac{1}{2}\Big(\text{sign}(\widehat{S} - \nu S) + S\Big)$.
$n \leftarrow \text{rowlength}(\alpha)$
$m \leftarrow \text{columnlength}(\alpha)$
$A \leftarrow [0]_{n\times m}$
$C \leftarrow [0]_{n\times m}$
while $\max(R) \neq 0$ **do**
  For each (i, j) such that $R(i, j) \neq 0$, do
  $S' \leftarrow \{S : S(i, j) \leftarrow 0, S(j, i) \leftarrow 0\}$
  Compute T and b, given S' and $\alpha^{(\mu)}$
  $\bar{K} \leftarrow \sum_{i=1}^{n}\sum_{\mu=1}^{m} K_{i\mu}$
  for $\mu = 1$ to m **do**
    for $t = 1$ to n **do**
      $C(t, \mu) \leftarrow \alpha_t^{(\mu)}\big(\sum_{j=1}^{n} T_{tj}\alpha_j^{(\mu)} + b_t\big)$
    end for
  end for
  if $\bar{K} > \kappa \wedge \min C > 1$ **then**
    $A(i, j) \leftarrow \bar{K}$; $A(j, i) \leftarrow \bar{K}$
  end if
  $R(i, j) \leftarrow 0$; $R(j, i) \leftarrow 0$
end while

for deletion is outlined using pseudo-code in Algorithm 1.

Upon completion, Algorithm 1 provides the (nonzero) performance indices of the network produced after each potential expensive edge which may be trimmed. The (i, j) edge with the highest $\bar{K}$ value in A is removed from the network and the process is repeated until the while loop of Algorithm 1 is no longer executed (R is a matrix of zeros). Then either all edges with cost above the threshold ν are removed, or their removal violates the stability condition (2), or results in an unacceptable performance metric. Although, in general, performance deteriorates as more neuron links are removed, it is possible that one may actually increase the objective function using fewer network connections.

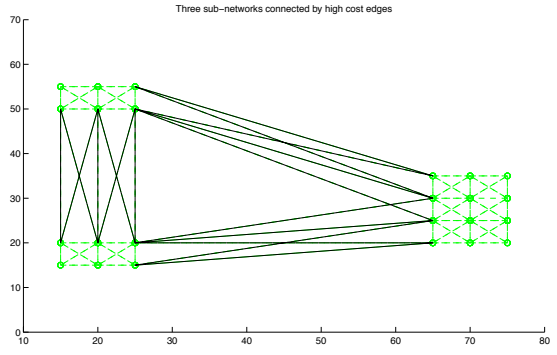


Fig. 1. A CNN interconnection structure where high cost edges are stretched and highlighted

IV. A Design Example and Simulations

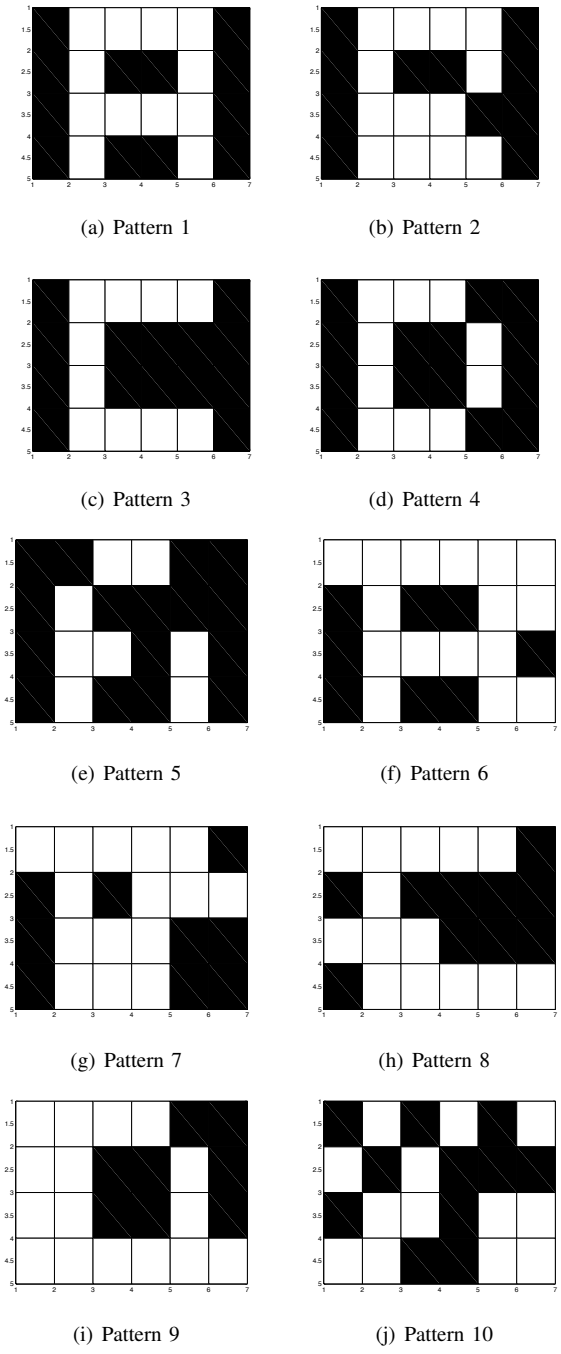
As an example, and proof of efficiency of suggested method, we use a CNN that is made up of $n = 24$ cells The schematic of the network topology is shown in Fig. 1. As can be seen, the network is divided into three small sub-networks which are linked by costly edges marked by solid lines. These costs are represented in form of a weighted adjacency matrix $\widehat{S}$ in eq. (7) such that the costly edges have a higher weight.

$$\widehat{S} = \begin{vmatrix} 0&1&0&0&0&0&1&1&0&0&0&0&0&0&0&0&0&0&0&0&0&0&0&0 \\ 1&0&1&0&0&0&1&1&1&0&0&0&0&0&0&0&0&0&0&0&0&0&0&0 \\ 0&1&0&2&0&0&0&1&1&2&0&0&0&0&0&0&0&0&0&0&0&0&0&0 \\ 0&0&2&0&1&0&0&0&2&1&1&0&0&0&0&0&0&0&0&0&0&0&0&0 \\ 0&0&0&1&0&1&0&0&0&1&1&1&0&0&0&0&0&0&0&0&0&0&0&0 \\ 0&0&0&0&1&0&0&0&0&0&1&1&0&0&0&0&0&0&0&0&0&0&0&0 \\ 1&1&0&0&0&0&0&1&0&0&0&0&2&2&0&0&0&0&0&0&0&0&0&0 \\ 1&1&1&0&0&0&1&0&1&0&0&0&2&2&2&0&0&0&0&0&0&0&0&0 \\ 0&1&1&2&0&0&0&1&0&1&0&0&0&2&2&2&0&0&0&0&0&0&0&0 \\ 0&0&2&1&1&0&0&0&2&0&1&0&0&0&2&1&1&0&0&0&0&0&0&0 \\ 0&0&0&1&1&1&0&0&0&1&1&0&0&0&0&1&1&1&0&0&0&0&0&0 \\ 0&0&0&0&1&1&0&0&0&0&1&0&0&0&0&0&1&1&0&0&0&0&0&0 \\ 0&0&0&0&0&0&2&2&2&0&0&0&1&0&1&0&0&0&1&1&1&0&0&0 \\ 0&0&0&0&0&0&2&2&2&0&0&0&1&0&1&0&0&0&1&1&2&0&0&0 \\ 0&0&0&0&0&0&0&0&2&1&1&0&0&0&2&0&1&0&0&0&2&1&1&0 \\ 0&0&0&0&0&0&0&0&1&1&1&0&0&0&1&0&1&0&0&0&1&1&1&1 \\ 0&0&0&0&0&0&0&0&0&0&1&1&0&0&0&1&0&0&0&0&0&1&1&1 \\ 0&0&0&0&0&0&0&0&0&0&0&0&1&1&0&0&0&0&0&1&0&0&0&0 \\ 0&0&0&0&0&0&0&0&0&0&0&0&1&1&1&0&0&0&1&0&1&0&0&0 \\ 0&0&0&0&0&0&0&0&0&0&0&0&0&1&1&2&0&0&0&1&0&2&0&0 \\ 0&0&0&0&0&0&0&0&0&0&0&0&0&2&1&1&0&0&0&2&0&1&0&0 \\ 0&0&0&0&0&0&0&0&0&0&0&0&0&0&1&1&1&0&0&0&1&0&1&1 \\ 0&0&0&0&0&0&0&0&0&0&0&0&0&0&0&1&1&0&0&0&0&1&0&0 \end{vmatrix}, \quad (7)$$

The binary patterns chosen as memory vectors are shown in Fig. 2. To keep things simple, we first consider only top four memory patterns as input vectors and test our proposed method. Each memory vector corresponds to a 6×4 array with black and white boxes represented numerically by -1 and 1 respectively, and is read left to right from top left corner to produce a single column, stack vector of binary values. The network parameters namely the bias vector b and the weight matrix $T = \widetilde{T} + I_n$, are obtained by solving the GEVP with the bounds in system (4) set to $L = 1$ and $U = 10$.

To reduce the communication cost, connection edges from the given set of costly edges marked by solid lines in Fig. 1 are considered for possible removal. These edges can be numerically identified by setting a cost threshold ν and evaluating the residual matrix $R = \frac{1}{2}\Big(\text{sign}(\widehat{S} - \nu S) + S\Big)$. Successive trimming of costly edges results in a different $\bar{K}$ value. Deletion of nine costly edges in the sequence suggested by this method results in a maximum stability parameter sum of 108.3362 in the example considered.

The performance of proposed design method is quantified in terms of average recall probability of the neural network. We estimate the recall probabilities by generating a set of 50 different arrays of random noise and contaminating to each memory vector with these arrays to produce 50 different perturbed input memory vectors for each given memory pattern α^{μ}. Also for $i = 1, \ldots, n$, the perturbed input memory vectors always satisfy $|x_i(0)| \leq 1$. For each initial condition vector α^{μ}, the corresponding set of perturbed condition vectors are fed to the network and those perturbed condition vectors which result in same output as the initial condition vector are recorded. A comparison is made between the original network model and the network with the topology determined by the optimization algorithm



(a) Pattern 1 (b) Pattern 2

(c) Pattern 3 (d) Pattern 4

(e) Pattern 5 (f) Pattern 6

(g) Pattern 7 (h) Pattern 8

(i) Pattern 9 (j) Pattern 10

Fig. 2. Ten patterns to be stored as memory vectors

(shown in Fig. 3). It can be seen that the original network topology has same recall probability graph as the sparser topology derived with selective deletion of costly edges. However with a relatively large number of deleted edges, there is a noticeable decrease in network performance. The objective of optimization is thus to balance communication cost versus average recall probability.

Figure 4 shows the effect on recall probability of the CNN parameterized by the number of edges deleted. The vertical axis represents the average of the recall probabilities for

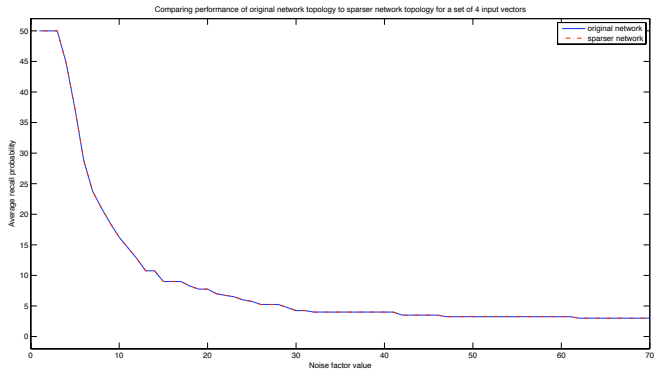


Fig. 3. Network performance vs. selective deletion of edges for the original and the optimized network. The curves are initially indistinguishable.

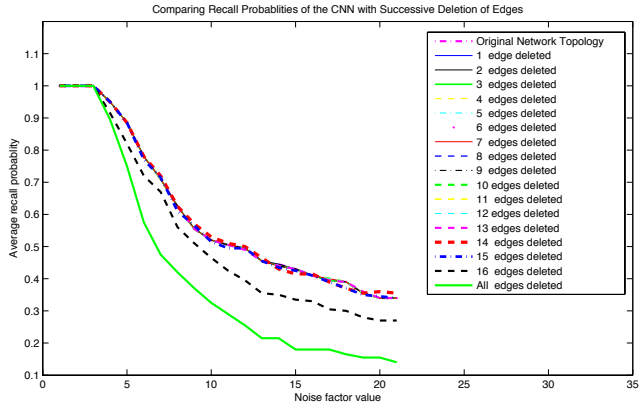


Fig. 4. Network performance, parameterized by number of edges removed.

the four given initial condition vectors and the horizontal axis represents the noise factor or the amplification factor by which the set of noise is multiplied. There is always a gradual decrease in performance as the amount of noise injected in the pattern increases. The recall probability of the original network design evolves with the amount of noise very similar to that of the sparser network produced through the topology optimization process. The additional curves correspond to even sparser networks, and demonstrate that reducing the connectivity further, has an observable impact on network performance. Also if the new $\bar{K}$ value is close to the original $\bar{K}$, performance is still comparable. But when $\bar{K}$ drops significantly compared to its original value, we see a notable performance degradation.

Next we consider all ten input patterns shown in Fig. 2 to test the ability of the network to store additional patterns with lean interconnection topologies. A comparison is made between the original network model and the network with the topology determined by the optimization algorithm (shown in Fig. 5) for a set of all ten input memory vectors. Simulation results again show that the original network topology has same recall probability graph as the sparser topology derived with selective deletion of costly edges. This proves the validity of the above proposed method in presence of larger set of input data to be stored. It is also observed that the

number of input vectors to be stored is increased, the recall probability of the network decreases gradually and it is able to tolerate less noise (Fig. 6). This is to be expected, since more attractors now share the same area of the state space; the region of attraction for each one of these stable equilibria is reduced.

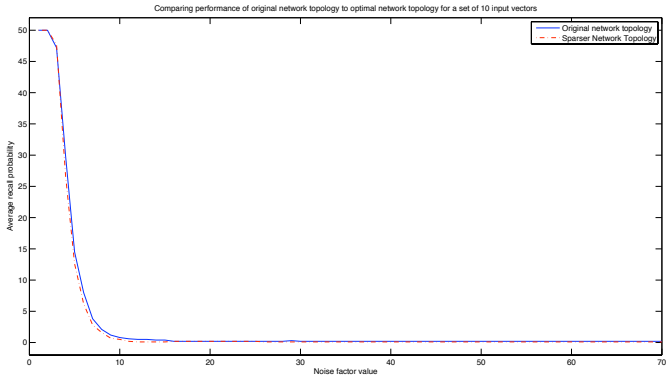


Fig. 5. Network performance, original to sparser comparison for 10 memory vectors.

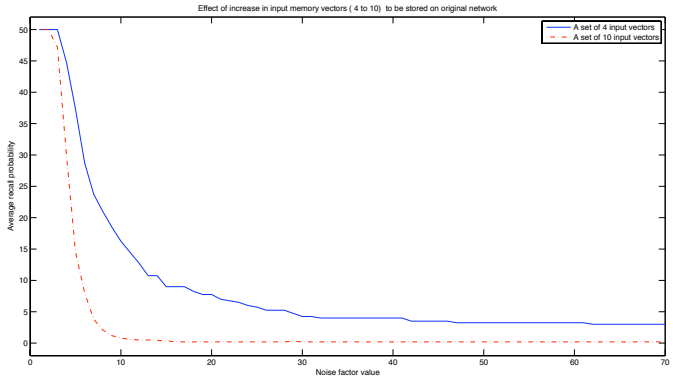


Fig. 6. Original network performance for different set of input vectors.

V. Conclusions

This paper suggests a topology optimization approach to cellular neural network design, as a method for realizing associative memories using sparser (thus more efficient, or less expensive) networks. The optimization criterion utilizes the stability parameters of the network as a quantitative measure of its ability to recall patterns contaminated with different levels of noise. These stability parameters have been associated with the size of the attraction regions of the neural network. Simulations and comparisons show that a sparser topology can be achieved without significantly degrading the performance of the network, by selectively deleting those weights from the optimized network which contribute the least (as measured by the arithmetic sum of the stability parameters) to ability of the network to recall the desired patterns.

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